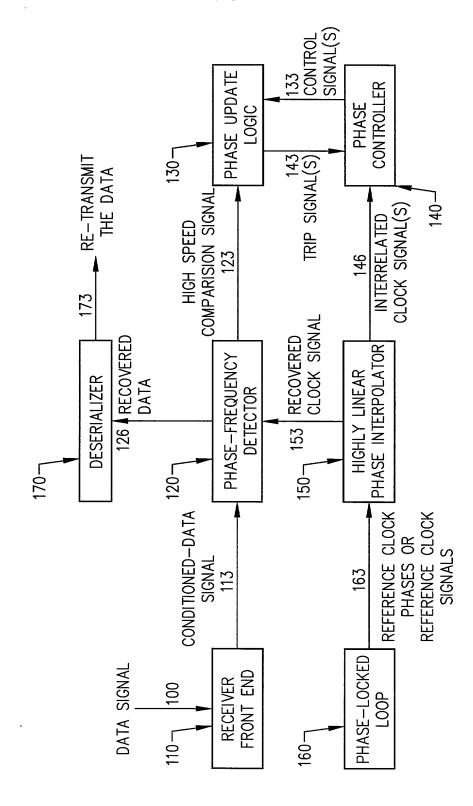
FIGURE 1

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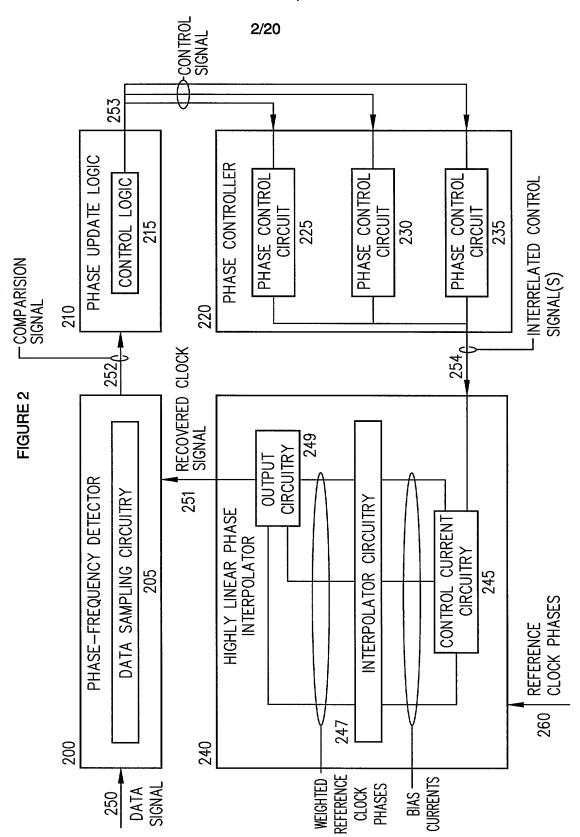


FIGURE 3A

42390P12280 PHASE INTERPOLATOR ROBERT C. GLENN, et al.

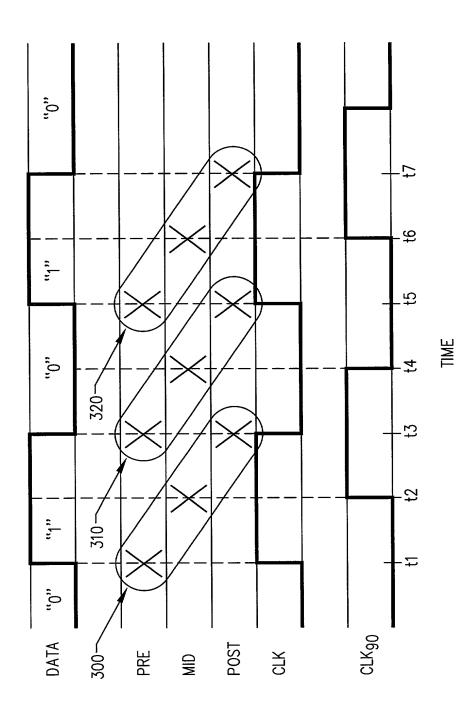


FIGURE 3B

42390P12280 PHASE INTERPOLATOR ROBERT C. GLENN, et al.

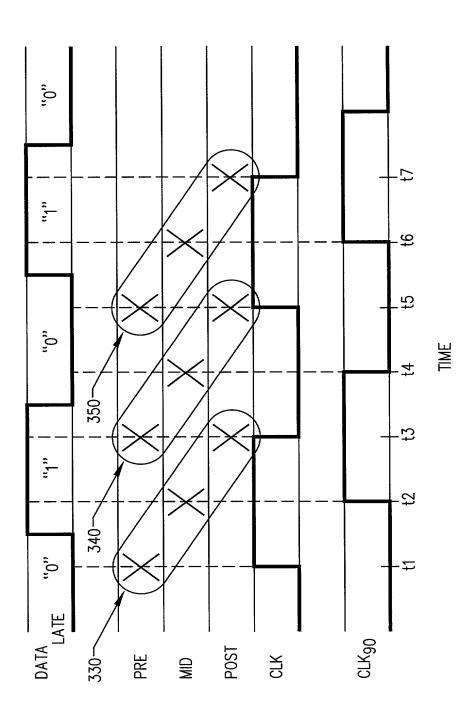
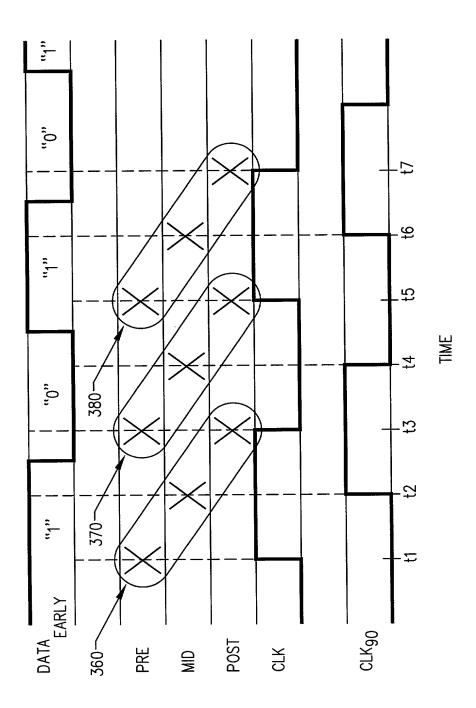
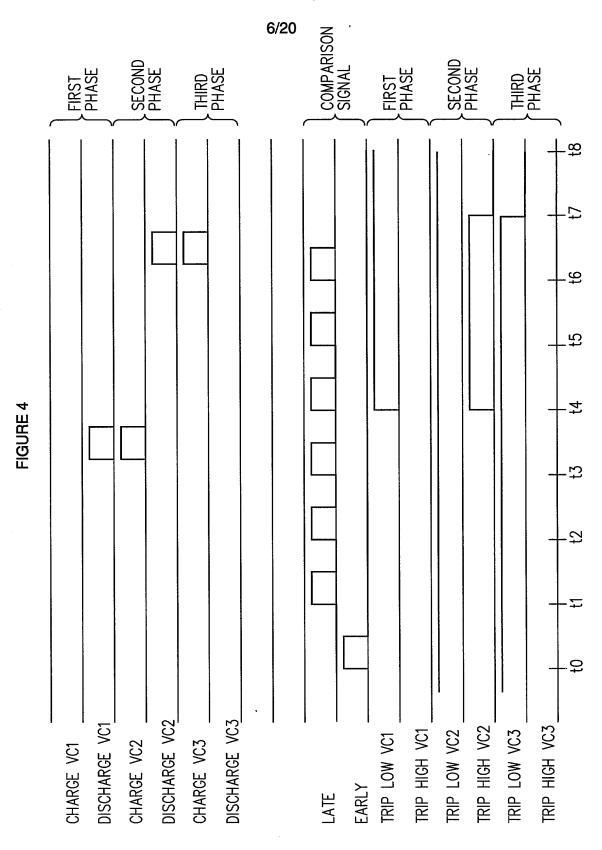


FIGURE 3C

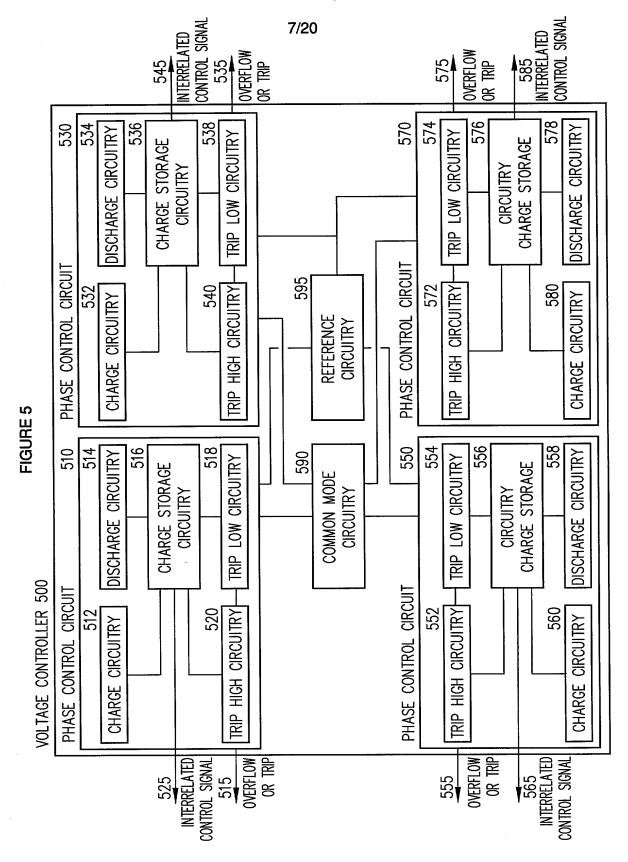
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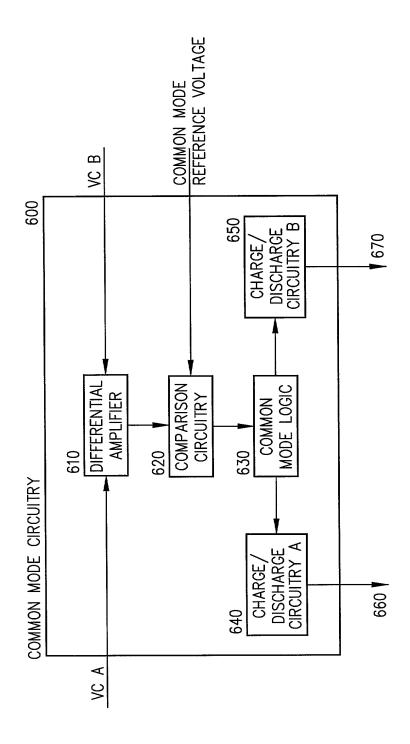
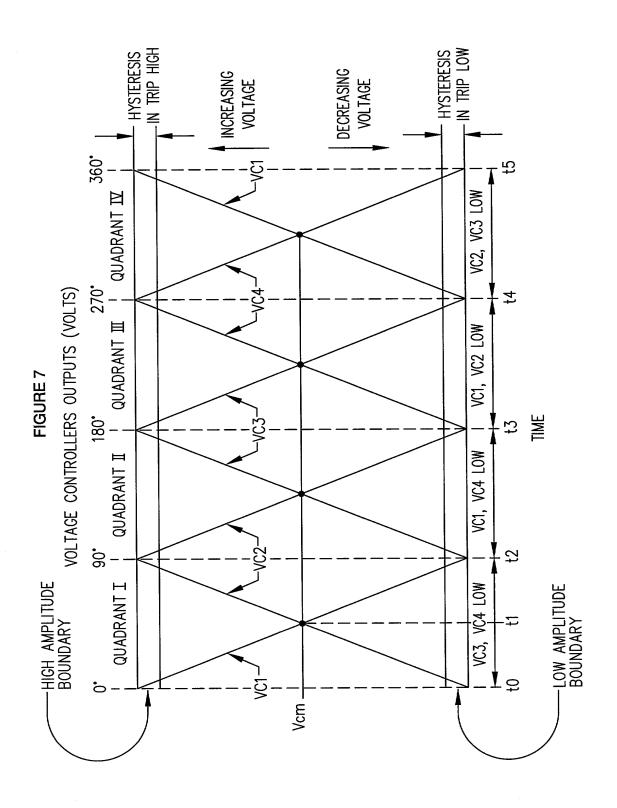
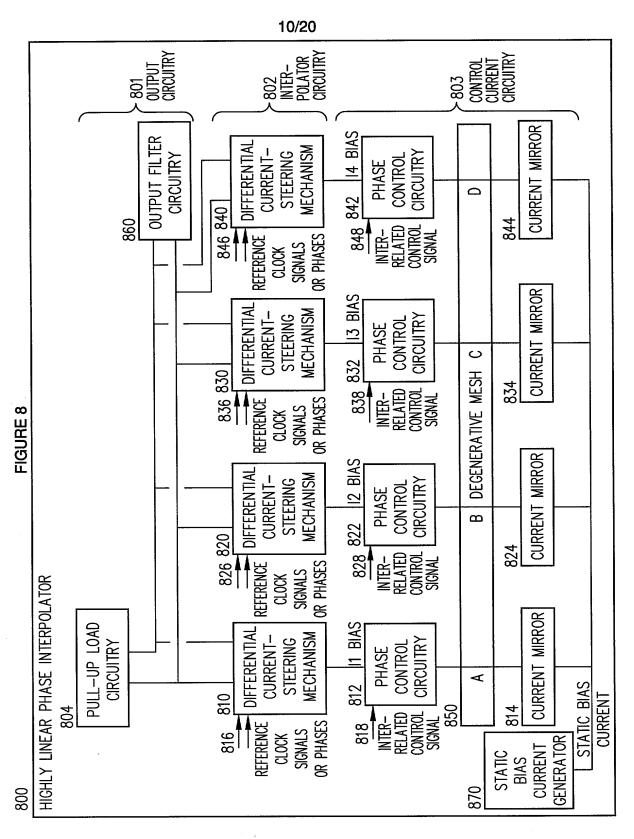


FIGURE 6

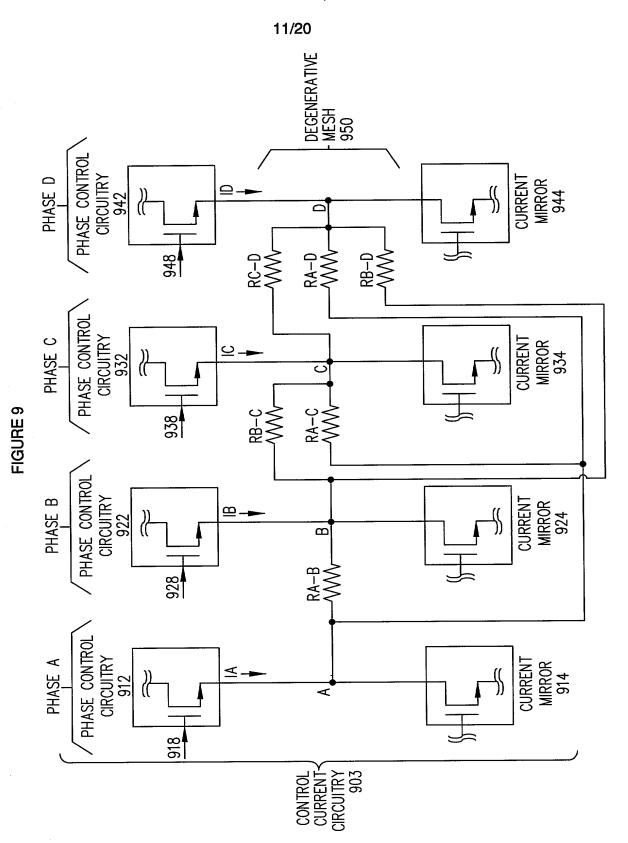
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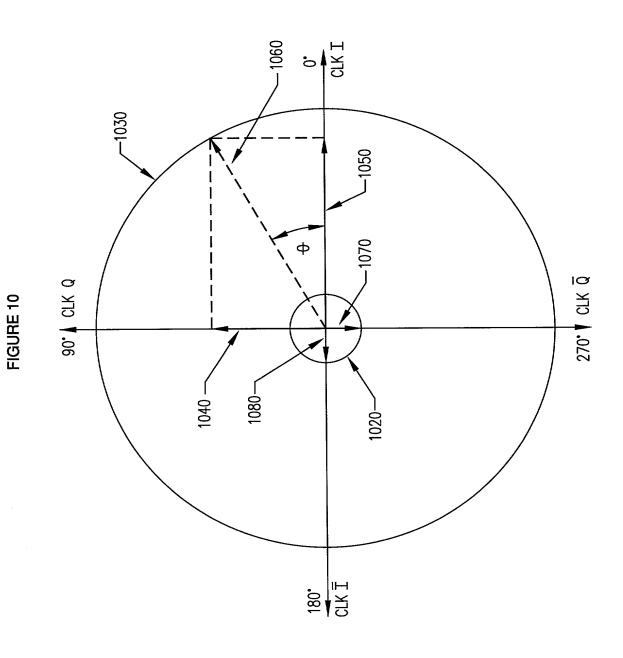
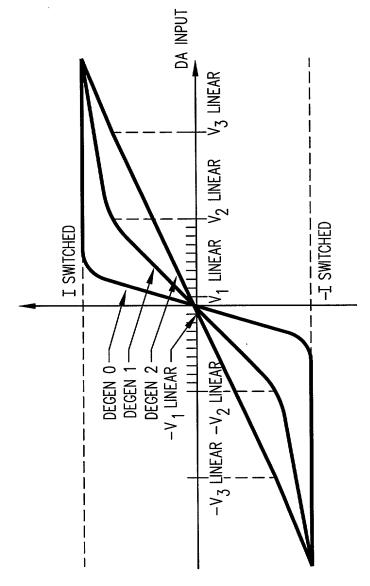


FIGURE 11

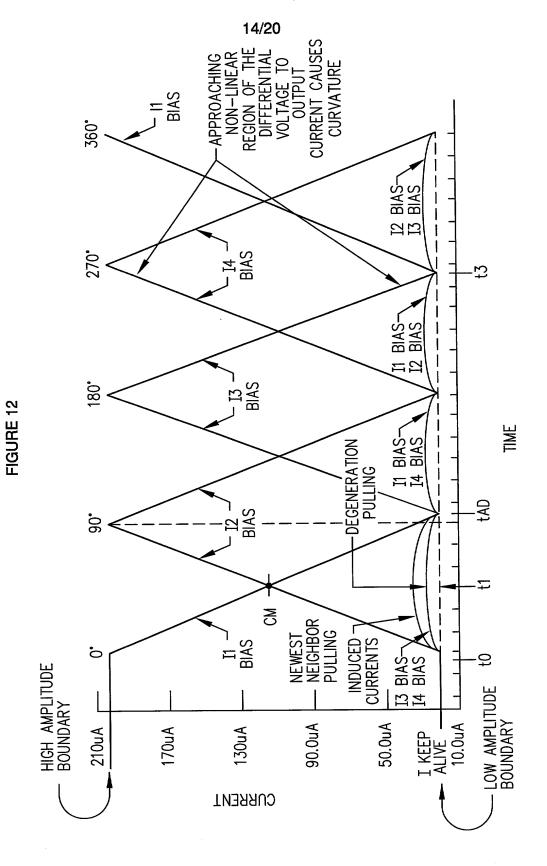
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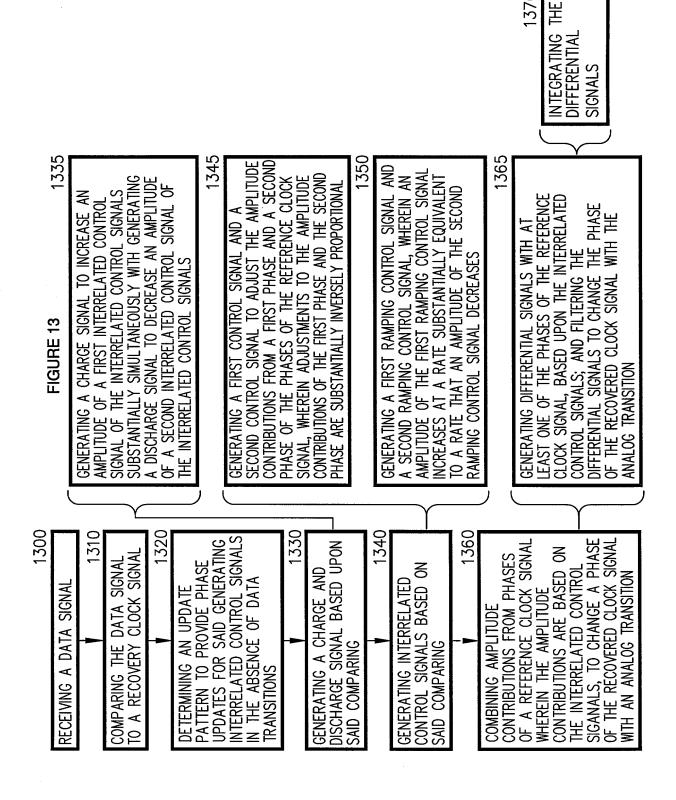
PSEUDO-DIFFERENTIAL AMPLIFIER SWING RANGE BASED ON DEGENERATION

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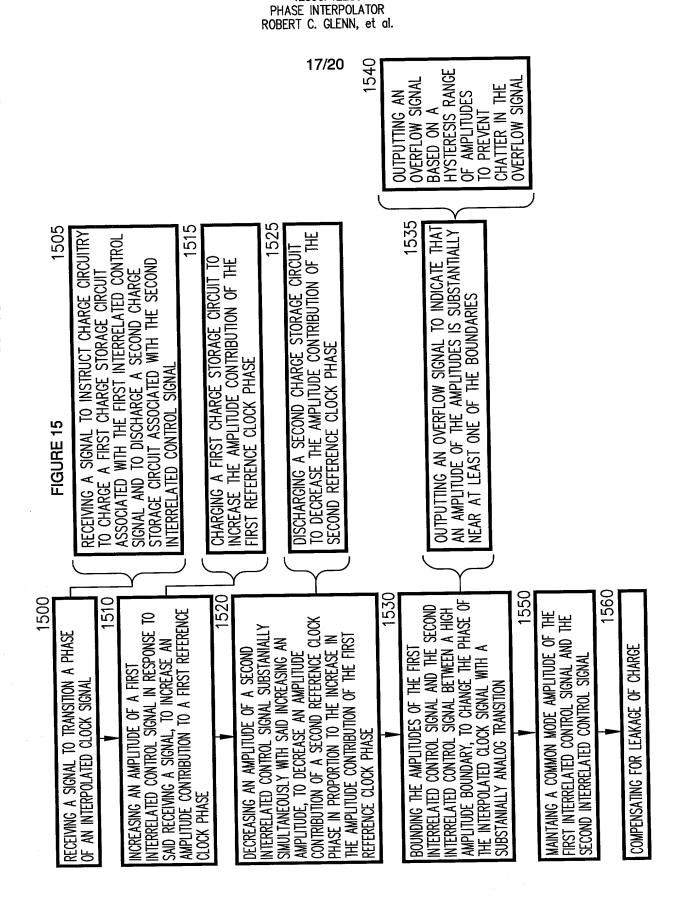
1370



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1430 1440 TO CHANGE A PHASE OF THE RECOVERED CLOCK BASED ON THE INTERRELATED CONTROL SIGNALS, WHEREIN THE AMPLITUDE CONTRIBUTIONS ARE COMBINING AMPLITUDE CONTRIBUTIONS FROM SIGNALS BASED ON SAID COMPARING GENERATING INTERRELATED CONTROL PHASES OF A REFERENCE CLOCK SIGNAL SIGNAL WITH AN ANALOG TRANSITION 1420 1410 COMPARING THE DATA SIGNAL TO A RECOVERED CLOCK SIGNAL RECEIVING A DATA SIGNAL MACHINE-READABLE MEDIUM 1400

FIGURE 14

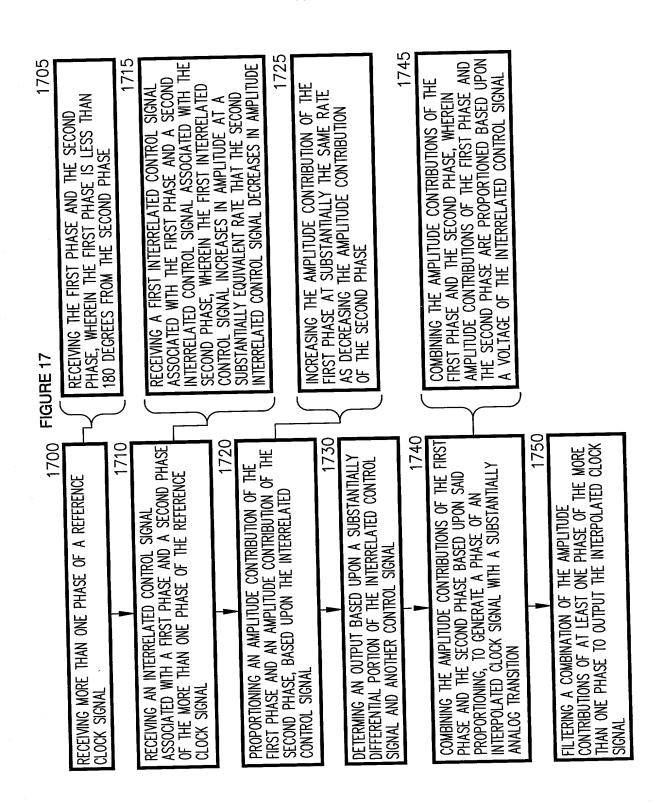


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1640 1630 INTERRELATED CONTROL SIGNAL AND THE SECOND INTERRELATED CONTROL SIGNAL BETWEEN A HIGH PHASE IN PROPORTION TO THE INCREASE IN THE CONTRIBUTION OF A SECOND REFERENCE CLOCK INTERRELATED CONTROL SIGNAL SUBSTANTIALLY AMPLITUDE BOUNDARY AND A LOW AMPLITUDE BOUNDARY, TO CHANGE THE PHASE OF THE SIMULTANEOUSLY WITH SAID INCREASING AN Bounding the amplitudes of the First DECREASING AN AMPLITUDE OF A SECOND AMPLITUDE, TO DECREASE AN AMPLITUDE AMPLITUDE CONTRIBUTION OF THE FIRST REFERENCE CLOCK PHASE INTERPOLATED CLOCK SIGNAL WITH A SUBSTANTIALLY ANALOG TRANSITION 1620 1610 AMPLITUDE CONTRIBUTION OF A FIRST REFERENCE PHASE OF AN INTERPOLATED CLOCK SIGNAL INTERRELATED CONTROL SIGNAL IN RESPONSE TO SAID RECEIVING A SIGNAL, TO INCREASE AN RECEIVING A SIGNAL TO TRANSITION A INCREASING AN AMPLITUDE OF A FIRST MACHINE-READABLE MEDIUM CLOCK PHASE

FIGURE 16



MACHINE-READABLE MEDIUM	
1810	1830
RECEIVING MORE THAN ONE PHASE OF A REFERENCE CLOCK SIGNAL	PROPORTIONING AN AMPLITUDE CONTRIBUTION OF THE FIRST PHASE AND AN AMPLITUDE
	UPON THE INTERRELATED CONTROL SIGNAL
1820	1840
RECEIVING AN INTERRELATED CONTROL SIGNAL ASSOCIATED WITH A FIRST PHASE AND A SECOND PHASE OF THE MORE THAN ONE PHASE OF THE REFERENCE CLOCK SIGNAL	COMBINING THE AMPLITUDE CONTRIBUTIONS OF THE FIRST PHASE AND THE SECOND PHASE BASED UPON SAID PROPORTIONING, TO GENERATE A PHASE OF AN INTERPOLATED CLOCK SIGNAL WITH
יבן בויבויטר טרטטי טטייה	A SUBSTANIALLY ANALOG TRANSITION